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incl.

19 a current source providing a current to a drain of said  
20 seventh FET; and  
21 a capacitor having one plate connected to the common  
22 connection node for said current source and said seventh FET, and  
23 having another plate connected to the common gate connection node  
24 of one of said first current mirror and said second current mirror.

### REMARKS

Reconsideration of the above-identified Application is respectfully requested. Claims 1 - 9 are in the case. All were rejected. [Claims 1 - 8 have been canceled.] Claim 9 has been amended. Claims 10 - 13 have been added. The specification was objected to. The specification was amended.

Preliminarily, as a matter of the status of the case, it is noted that a Request for Corrected Filing Receipt was filed on October 28, 1998. The originally issued Filing Receipt incorrectly shows the filing date as 01/06/98, while it should be 10/06/98, and it includes a misspelling of a word in the title. The Return Receipt post card, having an official stamp showing that such Request was received at the United States Patent and Trademark Office on November 2, 1998, was received by return mail by this office. No action on that Request has been received by this office, as of the date of the filing of this paper. Applicant hereby requests prompt notification of the status of that Request.

Regarding the objection to the disclosure, the specification has been amended to correct the informalities upon which this objection was based. Accordingly, withdrawal of this objection is respectfully requested.

Regarding the rejection of Claims 1 - 9 under 35 U.S.C. § 102(b) as allegedly being anticipated by Schade Jr., Claims 1 - 8 have been canceled,

thereby rendering this rejection moot with respect thereto, and Claim 9 has been amended to distinguish over Schade. Claim 9 is directed to a Miller-compensated amplifier including a capacitor coupled between the amplifier output node and an internal node of a first amplifier stage, wherein the capacitor is coupled so as to provide voltage-mode gain. The patent to Schade apparently relates to an operational transconductance amplifier, but to one that is not Miller-compensated. Being Miller-compensated, the recited capacitor of the amplifier of Claim 9 has an additional voltage-mode gain, which constitutes the Miller compensation, which the capacitor of Schade does not have. This voltage-mode gain is now recited explicitly in Claim 9, and so distinguishes over Schade. Note that the nonlinear current gain nature of the Schade circuit make the predictability of the amplified capacitor less controlled, since it is nonlinear and a function of the current flowing through R11 and Q11. By contrast, in accordance with the invention as set forth in Claim 9, the current through the claimed capacitor is amplified, and the benefits of Miller compensation are provided as well. There is no teaching nor suggestion of this Miller-compensated structure in Schade, nor, in fact, in any of the art of record, either considered alone or in any combination. Accordingly, it is respectfully requested that this rejection be withdrawn.

New Claims 10 - 12, which depend from Claim 9, add further limitations, while new Claim 13 recites the invention in accordance with aspects more specific to the particular embodiments described in the specification. All of these newly added claims are patentable over the art of record as well.

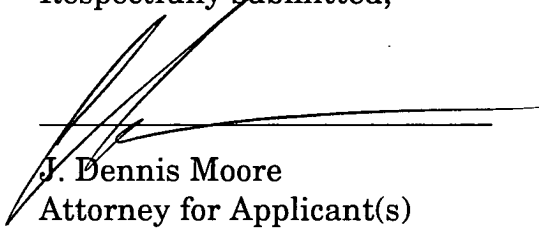
It is respectfully submitted that the claims recite the patentably distinguishing features of the invention and that, taken together with the above remarks, the present application is now in proper form for allowance. Reconsideration of the application, as amended, and allowance of the claims are requested at an early date.

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While it is believed that the instant amendment places the application in condition for allowance, should the Examiner have any further comments or suggestions, it is respectfully requested that the Examiner contact the undersigned in order to expeditiously resolve any outstanding issues.

To the extent necessary, the Applicants petition for an Extension of Time under 37 C.F.R. §1.136. Please charge any fees in connection with the filing of this paper, including extension of time fees to the Deposit Account No. 20-0668 of Texas Instruments Incorporated.

Respectfully submitted,



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